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	RULE			

APPLICANTS

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**** CONTINUING DATA *******

This application is a CON of PCT/JP02/08742 08/29/2002

**** FOREIGN APPLICATIONS *******

JAPAN 2001-262295 08/30/2001

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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 1	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 4
Verified and Acknowledged	Examiner's Signature Initials				

ADDRESS

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TITLE

Method of purging semiconductor manufacturing apparatus and method of manufacturing semiconductor device

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